

**CURRICULAR STRUCTURE OF  
THREE YEARS FULL-TIME DIPLOMA COURSE IN  
ELECTRONICS AND TELE COMMUNICATION ENGINEERING**



**WEST BENGAL STATE COUNCIL OF TECHNICAL EDUCATION**

(A Statutory Body under West Bengal Act XXI of 1995) "Kolkata Karigori Bhavan", 2nd Floor, 110 S. N. Banerjee Road,  
Kolkata – 700013

WEST BENGAL STATE COUNCIL OF TECHNICAL EDUCATION												
TEACHING AND EXAMINATION SCHEME FOR DIPLOMA IN ENGINEERING COURSES												
COURSE NAME: FULL TIME DIPLOMA IN ELECTRONICS & TELECOMMUNICATION ENGINEERING												
DURATION OF COURSE: 6 SEMESTERS												
SEMESTER: THIRD												
BRANCH: ELECTRONICS & TELECOMMUNICATION ENGINEERING												
SR. NO.	SUBJECT	CREDITS	PERIODS			EVALUATION SCHEME						
			L	TU	PR	INTERNAL SCHEME			ESE	PR	@TW	Total Marks
						TA	CT	Total				
1.	Network Analysis	3	4	-	-	10	20	30	70	-	-	100
2.	Analog Electronics -I	4	4	-	-	10	20	30	70	-	-	100
3.	Digital Electronics	3	4	-	-	10	20	30	70	-	-	100
4.	Electrical Machine	2	2	-	-	5	10	15	35	-	-	50
5.	Computer Programming Language	2	2	-	-	5	10	15	35	-	-	50
6.	Network Analysis Laboratory	2	-	-	3	-	-	-	-	75	-	75
7.	Analog Electronics Laboratory	2	-	-	3	-	-	-	-	100	-	100
8.	Digital Electronics Laboratory	2	-	-	3	-	-	-	-	75	-	75
9.	Electrical Machine Laboratory	1	-	-	2	-	-	-	-	50	-	50
10.	Computer Programming Language Laboratory	1	-	-	2	-	-	-	-	50	-	50
11.	Professional Practice - I	2	-	-	3	-	-	-	-	-	50	50
12.	Environmental Studies	-	1	-	-	-	-	-	-	-	50	-
	Total	24	17	-	16	40	80	120	280	350	50	800

**STUDENT CONTACT HOURS PER WEEK:33 hrs, (Teaching-15 weeks + Internal Exam-2 weeks )**

**THEORY AND PRACTICAL PERIODS OF 60 MINUTES EACH**

**ABBREVIATIONS: L- Lecture, TU- Tutorials, PR- Practical, TA- Teachers Assessment, CT- Class Test, ESE- End Semester Exam, @TW-Term Work**

TA (Teacher's assessment) = 10 marks: Attendance & surprise quizzes = 5 marks and Assignment & group discussion = 5 marks for CT= 20 Marks.

TA (Teacher's assessment) = 5 marks: Attendance & surprise quizzes + Assignment & group discussion = 5 marks for CT = 10 Marks.

**Environmental Studies is a non credit based subject and only internal theoretical examination of 50 marks will be conducted for testing performance on the subject.**

**Total Marks : 800**

Minimum passing for Sessional marks is 40%, and for theory subject 40%.

Assessment of Practical, Oral & term work to be done as per the prevailing norms of curriculum implementation & assessment.

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TEACHING AND EXAMINATION SCHEME FOR DIPLOMA IN ENGINEERING COURSES												
COURSE NAME: FULL TIME DIPLOMA IN ELECTRONICS & TELECOMMUNICATION ENGINEERING												
DURATION OF COURSE: 6 SEMESTERS												
SEMESTER: FOURTH												
BRANCH: ELECTRONICS & TELECOMMUNICATION ENGINEERING												
SR. NO.	SUBJECT	CREDITS	PERIODS			EVALUATION SCHEME						
			L	TU	PR	INTERNAL SCHEME			ESE	PR	@TW	Total Marks
						TA	CT	Total				
1.	Elementary Communication Engineering	4	4	1	.-	10	20	30	70	-	-	100
2.	Analog Electronics –II	3	4	-	-	10	20	30	70	-	-	100
3.	Consumer Electronics	3	3	-	-	10	20	30	70	-	-	100
4.	Microprocessor	3	4	-	-	10	20	30	70	-	-	100
5.	Elementary Communication Engineering Laboratory	2	-	-	3	-	-	-	-	75	-	75
6.	Analog Electronics-II Laboratory	2	-	-	3	-	-	-	-	75	-	75
7.	Consumer Electronics Laboratory	2	-	-	2	-	-	-	-	75	-	75
8.	Microprocessor Lab	2	-	-	3	-	-	-	-	75	-	75
9.	Development of Life Skill-II Laboratory	2	-	-	2	-	-	-	-	50	-	50
10.	Professional Practice – II	2	1	-	3	-	-	-	-	-	50	50
Total		25	16	1	16	40	80	120	280	350	50	800

**STUDENT CONTACT HOURS PER WEEK:33 hrs, (Teaching-15 weeks + Internal Exam-2 weeks )**

**THEORY AND PRACTICAL PERIODS OF 60 MINUTES EACH**

**ABBREVIATIONS: L- Lecture, TU- Tutorials, PR- Practical, TA- Teachers Assessment, CT- Class Test, ESE- End Semester Exam, TW- Team Work**

TA (Teacher’s assessment) = 10 marks: Attendance & surprise quizzes = 5 marks and Assignment & group discussion = 5 marks for CT= 20 Marks.

TA (Teacher’s assessment) = 5 marks: Attendance & surprise quizzes + Assignment & group discussion = 5 marks for CT = 10 Marks.

**Total Marks : 800**

Minimum passing for Sessional marks is 40%, and for theory subject 40%.

Assessment of Practical, Oral & Term Work to be done as per the prevailing norms of curriculum implementation & assessment.

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TEACHING AND EXAMINATION SCHEME FOR DIPLOMA IN ENGINEERING COURSES												
COURSE NAME: FULL TIME DIPLOMA IN ELECTRONICS & TELECOMMUNICATION ENGINEERING												
DURATION OF COURSE: 6 SEMESTERS												
SEMESTER: FIFTH												
BRANCH: ELECTRONICS & TELECOMMUNICATION ENGINEERING												
SR. NO.	SUBJECT	CREDITS	PERIODS			EVALUATION SCHEME						
			L	TU	PR	INTERNAL SCHEME			ESE	PR	@TW	Total Marks
						TA	CT	Total				
1.	Digital and Microwave Communication Engg.	3	4	1	-	10	20	30	70	-	-	100
2.	Electronics Measurement	3	3	-	-	10	20	30	70	-	-	100
3.	Industrial Electronics-I	2	2	1	-	5	10	15	35	-	-	50
4.	Microcontroller & Embedded System	3	3	-	-	10	20	30	70	-	-	100
5.	<u>Elective-I ( Select any one)</u> Computer Network-I Medical Electronics-I Digital Signal Processing-I Computer Hardware Maintenance-I	2	2	-	-	5	10	15	35	-	-	50
6.	Digital and Microwave Communication Engg. Laboratory	2	-	-	3	-	-	-	-	75	-	75
7.	Electronics Measurement Laboratory	1	-	-	2	-	-	-	-	75	-	75
8.	Industrial Electronics-I Laboratory	2	-	-	2	-	-	-	-	75	-	75
9.	Microcontroller and Embedded system Lab	2	-	-	2	-	-	-	-	75	-	75
10.	Elective- I Laboratory	1	-	-	2	-	-	-	-	50	-	50
11.	Industrial Project & Entrepreneurship Development	3	1	-	2	-	-	-	-	100	-	100
12.	Professional Practice – III	1	-	-	3	-	-	-	-	-	50	50
	Total	25	15	2	16	40	80	120	280	450	50	900

**STUDENT CONTACT HOURS PER WEEK:33 hrs, (Teaching-15 weeks + Internal Exam-2 weeks )**

<p><b>THEORY AND PRACTICAL PERIODS OF 60 MINUTES EACH</b></p> <p><b>ABBREVIATIONS: L- Lecture, TU- Tutorials, PR- Practical, TA- Teachers Assessment, CT- Class Test, ESE- End Semester Exam, TW-Term Work,</b></p> <p>TA (Teacher's assessment) = 10 marks: Attendance &amp; surprise quizzes = 5 marks and Assignment &amp; group discussion = 5 marks for CT= 20 Marks.</p> <p>TA (Teacher's assessment) = 5 marks: Attendance &amp; surprise quizzes + Assignment &amp; group discussion = 5 marks for CT = 10 Marks.</p> <p><b>Total Marks : 900</b></p> <p>Minimum passing for Sessional marks is 40%, and for theory subject 40%.</p> <p>Assessment of Practical, Oral &amp; term work to be done as per the prevailing norms of curriculum implementation &amp; assessment.</p>
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TEACHING AND EXAMINATION SCHEME FOR DIPLOMA IN ENGINEERING COURSES												
COURSE NAME: FULL TIME DIPLOMA IN ELECTRONICS & TELECOMMUNICATION ENGINEERING												
DURATION OF COURSE: 6 SEMESTERS												
SEMESTER: SIXTH												
BRANCH: ELECTRONICS & TELECOMMUNICATION ENGINEERING												
SR. NO.	SUBJECT	CREDIT S	PERIODS			EVALUATION SCHEME						Total Marks
			L	TU	PR	INTERNAL SCHEME			ESE	PR	@TW	
						TA	CT	Total				
1.	Industrial Management	3	3	-	-	10	20	30	70	-	-	100
2.	Advance Communication Engineering	3	3	1	-	10	20	30	70	-	-	100
3.	Instrumentation & Control	3	3	1	-	10	20	30	70	-	-	100
4.	Industrial Electronics-II	3	3	-	-	10	20	30	70	-	-	100
5.	<u>Elective-II ( Select any one)</u> Computer Network-II Medical Electronics-II Digital Signal Processing-II Computer Hardware Maintenance-II	2	2	-	-	5	10	15	35	-	-	50
6.	Communication Engineering Laboratory-III	2	-	-	3	-	-	-	-	50	-	50
7.	Instrumentation & Control Laboratory	1	-	-	2	-	-	-	-	50	-	50
8.	Industrial Electronics Laboratory-II	2	-	-	3	-	-	-	-	50	-	50
9.	Elective- II Laboratory	1	-	-	2	-	-	-	-	50	-	50
10.	Industrial Project	2	-	-	4	-	-	-	-	100	-	100
11.	Professional Practice – IV	2	-	-	3	-	-	-	-	-	50	50
12.	General Viva voce	2	-	-	-	-	-	-	-	-	100	100
	Total	26	14	2	17	45	90	135	315	300	150	900

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**Total Marks : 900**

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